Please amend the subject application as follows:

IN THE CLAIMS:

 (Previously Presented) An electronic device for a mask container that is adopted to carry a lithography mask between a first station for first processing and a second station for second processing at a later time point, said electronic device comprising:

a receiver unit to receive first data;

- a memory unit to temporarily store said first data:
- a processor unit to process an instruction that uses said first data and to provide second data, the second data indicating how the second station uses the lithography mask for second processing to determine final quality and quantity of said second processing; and
- a transmitter unit to transmit said second data.

Claim 2 (Previously canceled).

- (Previously Presented) The electronic device of claim 1, wherein said transmitter unit transmits said second data to said second station before said second station uses said lithography mask in said second process.
- (Original) The electronic device of claim 3, wherein said processor unit
 processes said first data by combining said first data with an instruction.
- (Original) The electronic device of claim 4, wherein said instruction is indicative on how said lithography mask is used in said second process.

- (Original) The electronic device of claim 4, wherein said receiver unit also receives said instruction.
- (Original) The electronic device of claim 6, wherein said receiver unit receives said first data at a first time point and receives said instruction at a second time point that comes later.
- (Previously Presented) The electronic device of claim 1, wherein using said lithography mask in any of said first processing and second processing comprises at least one of the following:

inserting said lithography mask into said container;

removing said lithography mask from said container,

in combination, inserting and removing multiple lithography masks to and from said container:

writing data to said lithography mask;

reading data from said lithography mask;

exposing a semiconductor wafer by sending electromagnetic radiation through ; said lithography mask;

storing said lithography mask;

transporting said lithography mask from one location to another location; manufacturine said lithography mask;

maintaining said lithography mask;

modifying said lithography mask by changing exposure properties;

damaging said lithography mask, disposing of said lithography mask, recycling said lithography mask;

testing and measuring the properties of said lithography mask;

assigning an identifier for said lithography mask;

assigning an identifier for a plurality of lithography masks; and transferring information that relates to said lithography mask from a first electronic device in a first container to a further electronic device in a further container.

- (Original) The electronic device of claim 1, wherein said receiver unit, said memory unit, said processor unit, and said transmitter unit are coupled by a bus.
- (Original) The electronic device of claim 1, wherein said receiver unit and said transmitter unit are implemented as a transceiver unit.
- 11. (Original) The electronic device of claim 10, wherein said transceiver unit is a wireless transceiver.
- 12. (Original) The electronic device of claim 11, wherein said wireless transceiver is a radio frequency transceiver.
- (Original) The electronic device of claim 11, wherein said wireless transceiver is an infra-red transceiver.
- 14. (Original) The electronic device of claim 1, wherein said memory unit is a non-volatile memory.
- (Original) The electronic device of claim 14, wherein said non-volatile memory is an EEPROM.

- (Original) The electronic device of claim 14, wherein said non-volatile memory is an SRAM.
- 17. (Previously Presented) The electronic device of claim 1 comprising a power supply coupled to the receiver unit, the memory unit, the processor and the transmitter unit that is implemented by at least a component of the following group: battery, photovoltaic element, thermal converter, and inductive power converter.
- 18. (Previously Presented) The electronic device of claim 1 wherein the electronic device is permanently attached to said mask container by an adhesive.
- 19. (Original) The electronic device of claim 1, wherein said processor unit and said memory unit are implemented on a single monolithic chip.
- 20. (Previously Presented) The electronic device of claim 10, wherein said transceiver unit communicates with a further electronic device at a further lithography mask container and with further stations.
- 21. (Previously Presented) The electronic device of claim 1, wherein for a further lithography mask carried in said mask container said second data is indicative on how a third station uses said further lithography mask for third processing.

Claim 22 (Canceled)

Claim 23 (Canceled)

JAN.28.2884 3:55PM CORPLAN 512 996 6853

NO.979 P.8

DOCKET NO. SC0112WD

Claim 24 (Canceled)

PAGE 89 * RCVD AT 1/20/2034 4:55:-61 PM (Eastern Standard Time) * SVR:USP TO -EFXRF-(-1) * DNIS:27/2005 * CSID:512 996 (653 * DURATION (mm-s):60-12